

AMD E1019

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of } Group Art Unit: 2829
Rodgers, David Michael, et al. } Examiner: Kobert, Russell Marc
Serial No. 09/825.027 }
Filed: April 2, 2002 }
For: CLOCKED BASED METHOD }
AND DEVICES FOR }
MEASURING VOLTAGE- }
VARIABLE CAPACITANCES }
AND OTHER ON-CHIP }
PARAMETERS }

RESPONSE TO RESTRICTION REQUIREMENT

Hon. Assistant Commissioner for Patents
Washington, D.C. 20231

Dear Sir:

In the Office Action mailed December 10, 2002 the Examiner has stated that the present Application contains claims directed to five distinct inventions; Invention I, Invention II, Invention III, Invention IV, and Invention V. Specifically, the Examiner contends that the first invention, Invention I, is represented by Claims 1-29 and is drawn to an apparatus, classified in class 324, subclass 765. The Examiner further contends that the second invention, Invention II, is represented by Claims 30-72 and is drawn to methods, classified in class 324, subclass 765. The Examiner additionally contends that the third invention, Invention III, is represented by Claims 73-85 and is drawn to an apparatus, classified in

Serial No. 09/825,027

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Art Unit: 2829